TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX86F,TC74LCX86FN,TC74LCX86FT,TC74LCX86FK

Low-Voltage Quad 2-Input Exclusive OR Gate with 5-V Tolerant Inputs and Outputs

The TC74LCX86 is a high-performance CMOS exclusive OR gate. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

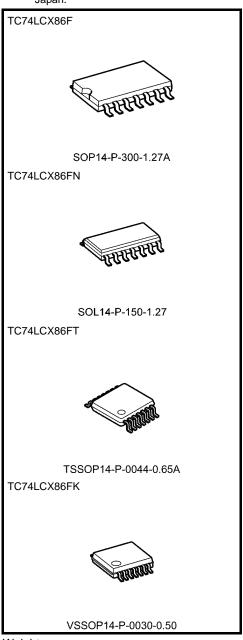
The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs.

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: VCC = 2.0 to 3.6 V
- High-speed operation: $t_{pd} = 6.5 \text{ ns (max) (V}_{CC} = 3.0 \text{ to } 3.6 \text{ V)}$
- Output current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: -500 mA
- Available in JEDEC SOP, JEITA SOP and TSSOP
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 86 type

Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

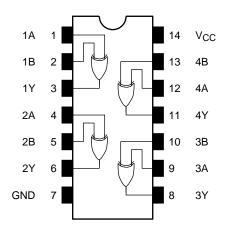
 SOP14-P-300-1.27A
 : 0.18 g (typ.)

 SOL14-P-150-1.27
 : 0.12 g (typ.)

 TSSOP14-P-0044-0.65A
 : 0.06 g (typ.)

 VSSOP14-P-0030-0.50
 : 0.02 g (typ.)

Pin Assignment (top view)



IEC Logic Symbol

	(1)		_	
1A		= 1	(3)	
1B	(2)		1Y	•
	(4)			
2A			(6)	,
2B	(5)		2Y	
	(9)		(0)	
3A	(10)		(8) 3Y	,
3B				
4A	(12)		(11)	
	(13)			•
4B				

Truth Table

А	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	-0.5 to 7.0	V	
DC input voltage	V _{IN}	-0.5 to 7.0	٧	
		-0.5 to 7.0 (Note 2)		
DC output voltage	Vouт	-0.5 to $V_{CC} + 0.5$ (Note 3)	V	
Input diode current	I _{IK}	-50	mA	
Output diode current	lok	±50 (Note 4)	mA	
DC output current	lout	±50	mA	
Power dissipation	PD	180	mW	
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA	
Storage temperature	T _{stg}	-65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: $V_{CC} = 0 V$

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND, V_{OUT} > V_{CC}$



Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	Vcc	2.0 to 3.6	V
rower supply voltage	VCC	1.5 to 3.6 (Note 2)	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	Vout	0 to 5.5 (Note 3)	V
Output voltage		0 to V _{CC} (Note 4)	V
Output current	I _{OH} /I _{OI}	±24 (Note 5)	mA
Output current	IOH/IOL	±12 (Note 6)	ША
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Note 2: Data retention only

Note 3: $V_{CC} = 0 V$

Note 4: High or low state

Note 5: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 6: $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$

Note 7: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristics		Symbol	Test Condition V _{CC} (V)		Min	Max	Unit	
<u> </u>								
Input voltage	H-level	V _{IH}	_		2.7 to 3.6	2.0	_	V
iliput voltage	L-level	V _{IL}	_		2.7 to 3.6	_	0.8	V
			$V_{IN} = V_{IH}$ or V_{IL}	I _{OH} = -100 μA	2.7 to 3.6	V _{CC} - 0.2	_	
	H-level	Voн		I _{OH} = -12 mA	2.7	2.2	_	
				I _{OH} = -18 mA	3.0	2.4	_	
Output voltage				I _{OH} = -24 mA	3.0	2.2	_	
	L-level V _{OL}		V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	2.7 to 3.6	_	0.2	
		Va		I _{OL} = 12 mA	2.7	_	0.4	
		VOL		I _{OL} = 16 mA	3.0	_	0.4	.]
			I _{OL} = 24 mA	3.0	_	0.55		
Input leakage current		I _{IN}	V _{IN} = 0 to 5.5 V		2.7 to 3.6	_	±5.0	μΑ
Power-off leakage current		l _{OFF}	V _{IN} /V _{OUT} = 5.5 V		0	_	10.0	μА
Quiescent supply current		laa	V _{IN} = V _{CC} or GND		2.7 to 3.6	_	10.0	
		Icc	V _{IN} = 3.6 to 5.5 V		2.7 to 3.6	_	±10.0	μА
Increase in I _{CC} per input		Δlcc	V _{IH} = V _{CC} - 0.6 V		2.7 to 3.6	_	500	

3



AC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.7		7.0	- ns
1 Topagation delay time	t _{pHL}		3.3 ± 0.3	1.5	6.5	
Output to output skew	t _{osLH}	(Nata	2.7			- ns
Output to output skew	t _{osHL}	(Note)	3.3 ± 0.3		1.0	

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{PLHm} - t_{PLHn}|, t_{OSHL} = |t_{PHLm} - t_{PHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	_	3.3	7	pF
Output capacitance	C _{OUT}	_	0	8	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz (Note	3.3	25	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 \text{ (per gate)}$

AC Test Circuit

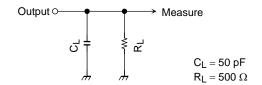


Figure 1

AC Waveform

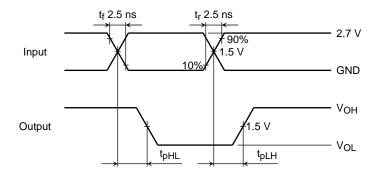
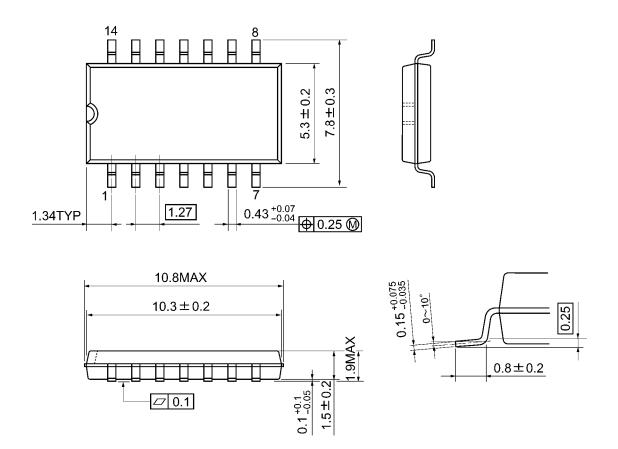


Figure 2 t_{pLH}, t_{pHL}

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Package Dimensions

SOP14-P-300-1.27A Unit: mm



6

Weight: 0.18 g (typ.)

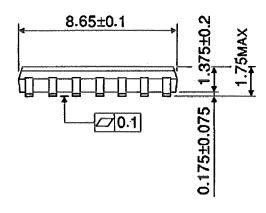
Unit: mm

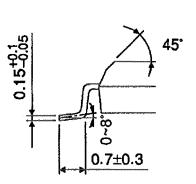


Package Dimensions (Note)

SOL14-P-150-1.27

14 日日日日日日 1-0+6: 1-0+6: 1-27 0.42±0.07 (中0.25 W)





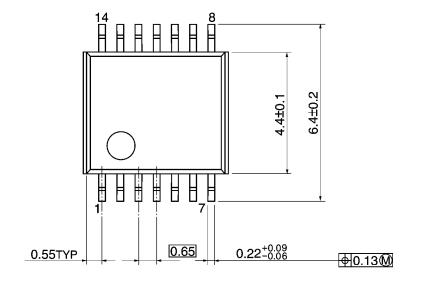
Note: This package is not available in japan.

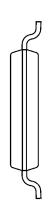
Weight: 0.12 g (typ.)

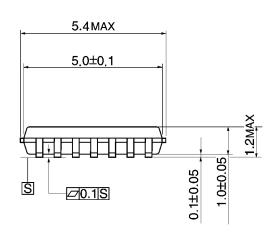
Package Dimensions

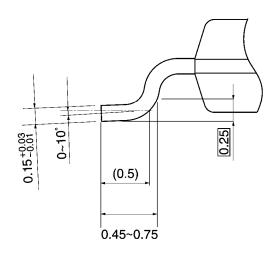
TSSOP14-P-0044-0.65A

Unit: mm





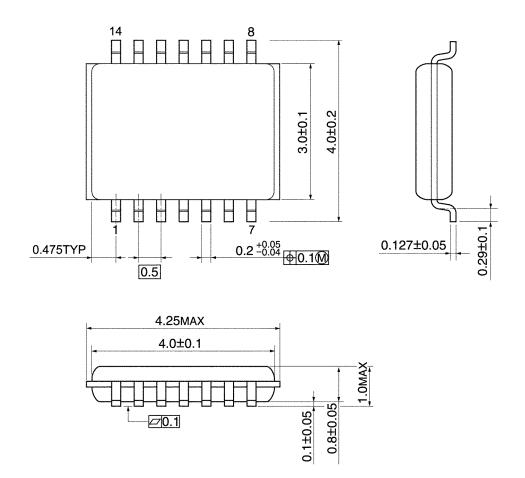




Weight: 0.06 g (typ.)

Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm



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Weight: 0.02 g (typ.)

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